ELET 4780 – Senior Project 1 – FALL 2015

To: Elias Kougianos

From: Chris Harris

CC: George Dunson

Date: 10/04/2015

Re: Senior Design Weekly Report – TEAM 1

# Overview

This week we received all our hardware from our first BOM. We were also able to gain access to the lab where we will be working on this. Our first try to get the Edison to talk with either or our computers turned out to be a problem, so George brought a computer so we can have a common machine to work on. He was able to get the Edison to talk to the computer on Friday and also found a distribution of Linux we can use with the Edison. We will upload this and continue to set up the Edison next week.

# Completed Tasks

* Got the Edison to talk to the computer.

# Ongoing Tasks

* Design custom PCB board.
* Create a functioning prototype.
* Get a distrobution of linux loaded onto the Edison.

# Next Week’s Tasks

* Begin prototype.
* Start design of PCB

# Interaction with the other team members

* We met on Tuesday for around an hour this week to gain access to the lab and set up the Edison. Worked on it again on Wednesday as well for around an hour. Met up between classes to discuss ideas.
* George came in on Friday to connect Edison to the terminal. I came in on Sunday to also work on the communication between Edison and the terminal.

# Unresolved major issues (if any)

* None.

# Weekly assignments (if any)

* None.